**Design and Simulation of Voltage Amplidyne System using Robust Control Technique**

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**Abstract:** In this paper, modelling designing and simulation of a simple voltage amplidyne system is done using robust control theory. In order to increase the performance of the voltage amplidyne system with H  optimal control synthesis and H  optimal control synthesis via -iteration controllers are used. The open loop response of the voltage amplidyne system shows that the system can amplify the input 7 times. Comparison of the voltage amplidyne system with H  optimal control synthesis and H  optimal control synthesis via -iteration controllers to track a desired step input have been done. Finally, the comparative simulation results prove the effectiveness of the proposed voltage amplidyne system with H  optimal control synthesis controller in improving the percentage overshoot and the settling time.

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**Keywords:** Amplidyne, H  optimal control synthesis controller, H  optimal control synthesis via -iteration controller

1. **Introduction**

An amplidyne is an electromechanical amplifier invented prior to World War II by Ernst Alexanderson. It consists of an electric powered motor riding a DC generator. The signal to be amplified is carried out to the generator's field winding, and its output voltage is an amplified reproduction of the field current. The amplidyne is used in enterprise in excessive power servo and manage systems, to increase low power manage signals to control powerful electric powered motors. An amplidyne incorporates an electric powered motor which turns a generator on the identical shaft. Unlike an ordinary motor-generator, the cause of an amplidyne isn't always to generate a consistent voltage but to generate a voltage proportional to an Input current, to modify the input. The motor affords the power, turning the generator at a constant velocity, and the signal to be amplified is applied to the generator's field winding.

1. **Mathematical Modelling of the system**

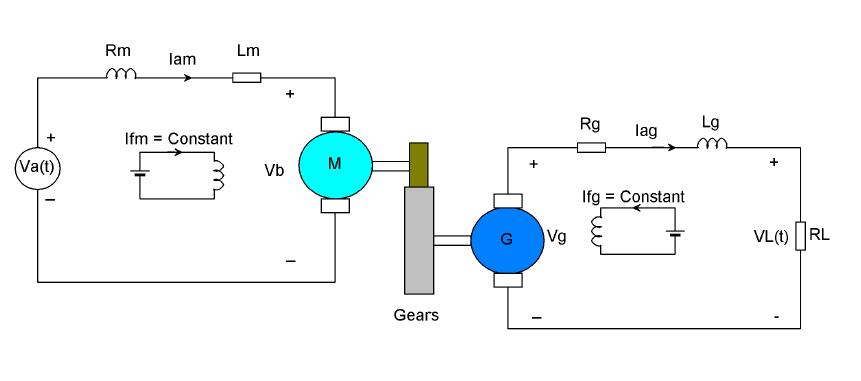


Figure 1 Voltage amplidyne system

**The Motor Side**

Assume

The stator current is constant therefore the magnetic flux is constant



The motor torque is proportional to the armature current and the flux



The voltage vb is proportional to the angular speed of the motor



Applying KVL to the motor circuit



Substituting Equation (3) in to Equation (4) yields:



The rotational equation of the motor is



Taking the Laplace Transform of Equation (5) and Equation (6)





Substituting Equation (8) into Equation (7) for Ia yields the transfer function between the input voltage and the output angular speed



**For the generator**

The field current is constant therefore the flux is constant

The generated voltage is proportional to the angular speed, flux and field current







Applying KVL to the generator circuit



Substituting Equation (10) in to Equation (12) yields:



Taking the Laplace Transform of Equation (13)



The transfer function between the input angular speed and the output current is



Simply the load voltage is



Substituting Equation (16) in to Equation (15) yields:



From the gear box relation



Substituting Equation (18) in to Equation (17) yields:



The transfer function between the input motor voltages to the output load voltage is found by combining Equation (9) and Equation (19) which results:



The parameters of the system is shown in Table 1 below.

Table 1 System parameter

|  |  |  |  |
| --- | --- | --- | --- |
| No | Parameter | Symbol | Value |
| 1 | Motor coil inductance |  | 18 H |
| 2 | Motor coil resistance |  | 27 ohm |
| 3 | Moment of inertia of the motor |  | 66 |
| 4 | Damping coefficient of the motor |  | 28 |
| 5 | Generator Coil inductance |  | 16 H |
| 6 | Generator coil resistance |  | 28 ohm |
| 7 | Load resistance |  | 100 ohm |
| 8 | Torque constant of the motor |  | 8 |
| 9 | Voltage constant of the motor |  | 16 |
| 10 | Voltage constant of the generator |  | 18 |
| 11 | Gear ratio |  | 55 |

The transfer function of the system becomes



And the state space representation becomes



**3.The Proposed Controllers Design2**

* 1. **H  Optimal Control Synthesis Controller Design**

**H**  optimal control synthesis solve the small-gain infinity-norm robust control problem; i.e., find a stabilizing controller F (s) for a system P (s) such that the closed-loop transfer function satisfies the infinity-norm inequality



The block diagram of the system with **H**  optimal control synthesis controller is shown in Figure 2 below

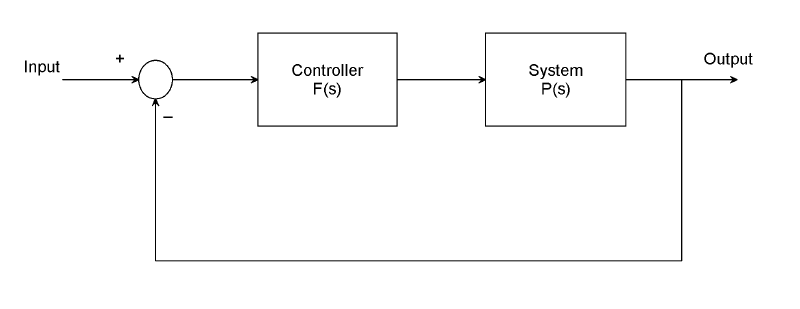


Figure 2 Block diagram of the system with **H**  optimal control synthesis controller

An important use of the infinity-norm control theory is for direct shaping of closed-loop singular value Bode plots of control systems. In such cases, the system P (s) will typically be the plant augmented with suitable loop-shaping filters.

The **H**  optimal control synthesis controller transfer function is



* 1. **H fOptimal Control Synthesis via -iteration Controller Design**

**H**  optimal control synthesis via -iteration compute the optimal **H**controller using the loop-shifting two-Riccati formulae. The output is the optimal “” for which the cost function can achieve under a preset tolerance.



The search of optimal stops whenever the relative error between two adjacent stable solutions is less than the tolerance specified. For most practical purposes, the tolerance can be set at 0.01 or 0.001. The block diagram of the system with **H**  optimal control synthesis via -iteration controller is shown in Figure 3 below

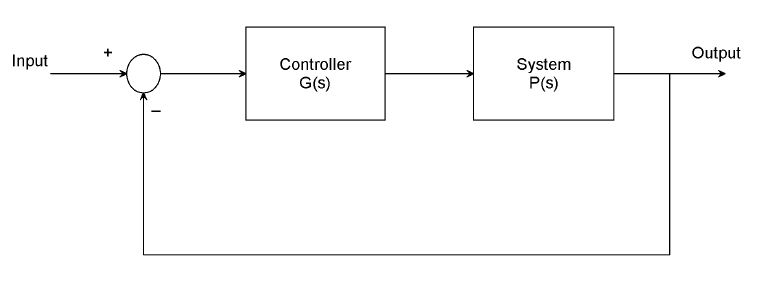


Figure 3 Block diagram of the system with **H**  optimal control synthesis via -iteration controller

The **H**  optimal control synthesis via -iteration controller transfer function is



1. **Result and Discussion**
   1. **Voltage Amplidyne System Open Loop Response**

The Simulink model of the open loop voltage amplidyne system and the simulation result of the system for a constant motor volt

age input of 30 volt is shown in Figure 4 and Figure 5 respectively.

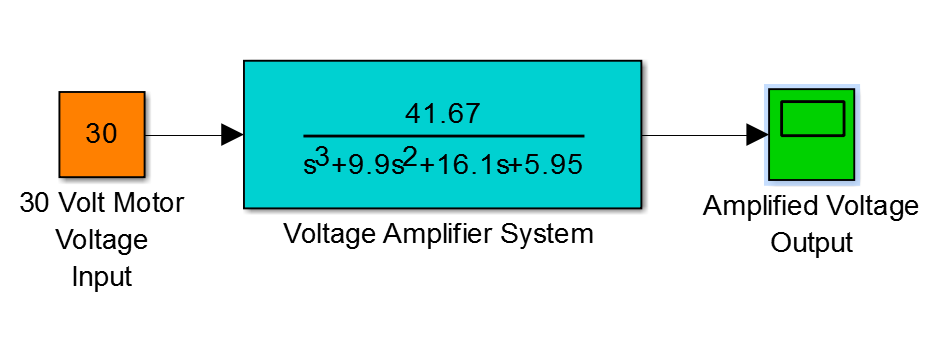


Figure 4 Simulink model of the open loop voltage amplidyne system

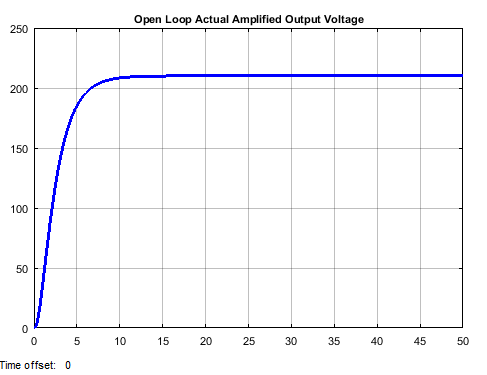


Figure 5 Simulation result

The simulation result shows that the amplidyne output voltage is 210 volt which amplifies the voltage 7 times.

* 1. **Comparison of the Proposed Controllers for Tracking a Desired Step Amplidyne Voltage**

The Simulink model of the voltage amplidyne system with **H**  optimal control synthesis and **H**  optimal control synthesis via -iteration controllers are shown in Figure 6 below

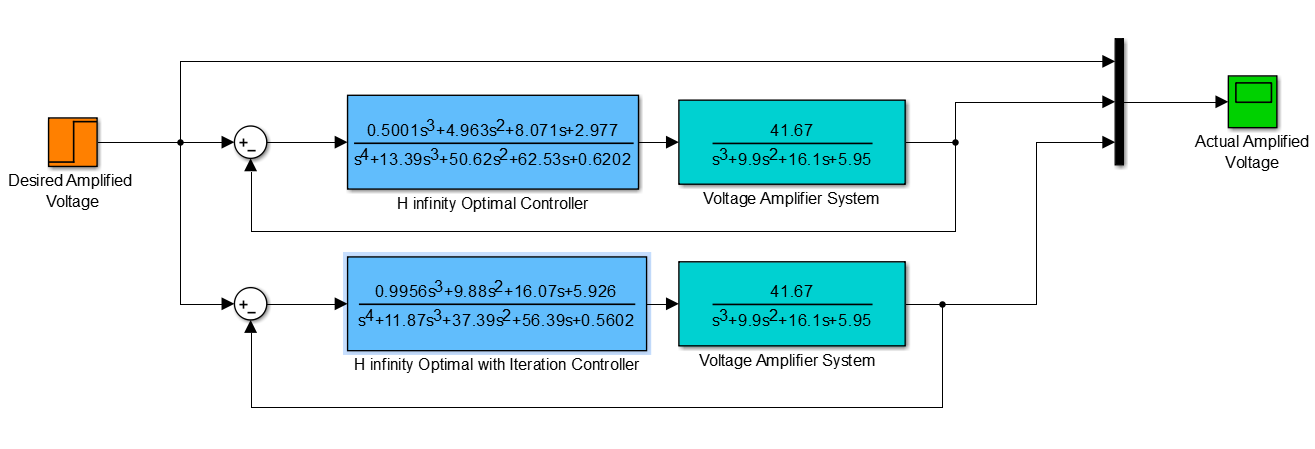


Figure 6 Simulink model of the voltage amplidyne system with **H**  optimal control synthesis and **H**  optimal control synthesis via -iteration controllers

The simulation result of the voltage amplidyne system with **H**  optimal control synthesis and **H**  optimal control synthesis via -iteration controllers for tracking a desired step (from 0 to 220 V) input is shown in Figure 7 below.

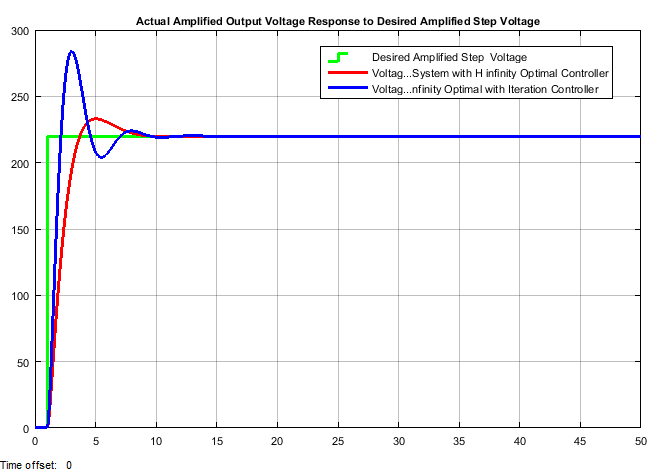


Figure 7 Simulation result

The performance data of the rise time, percentage overshoot, settling time and peak value is shown in Table 2.

Table 2 Step response data

|  |  |  |  |
| --- | --- | --- | --- |
| No | Performance Data | **H**  optimal | **H**  optimal via -iteration |
| 1 | Rise time | 2.1 sec | 2 sec |
| 2 | Per. overshoot | 4.54 % | 22.72 % |
| 3 | Settling time | 9 sec | 11 sec |
| 4 | Peak value | 1. V |  |

1. **Conclusion**

In this paper, a simple voltage amplidyne system is designed using a DC motor generator combination. In order to improve the performance of the system, a robust control technique with **H**  optimal control synthesis and **H**  optimal control synthesis via -iteration controllers are used. The open loop response of the system shows that a 7 time’s amplification voltage is gained. The comparison of the proposed controllers is done to track a desired step amplified voltage and the results proves that the system with **H**  optimal control synthesis controller improves the settling time and the percentage overshoot than the system with **H**  optimal control synthesis via -iteration controller.

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